



LED Device Structures with Minimized Light Re-Absorption

Tech ID: 22796 / UC Case 2007-670-0

BRIEF DESCRIPTION

A III-nitride light emitting diode (LED), in which light can be extracted from two surfaces of the LED before entering a shaped optical element and subsequently being extracted to air.

BACKGROUND

The LED structure affects how much light is emitted. In order to increase the light output power from the front side of the LED, conventional LEDs are typically equipped with a mirror placed on the backside of the substrate, or a mirror coating on the lead frame. However, this reflected light is re-absorbed by the active region of the LED, because the photon energy of emitted light is almost same as the band-gap energy of the light emitting materials. Due to this re-absorption of the emitted light by the active region, the net output power or the efficiency of the LED is decreased. Therefore, to achieve highly output power efficiency of the LED, device structures in which re-absorption of the light is minimized are desirable.

DESCRIPTION

Researchers at the University of California, Santa Barbara have developed a III-nitride light emitting diode (LED), in which light can be extracted from two surfaces of the LED before entering a shaped optical element and subsequently being extracted to air. This technology minimizes the light re-absorption at the LED active region by eliminating light reflection at the p-type side surface of the LED chip.

ADVANTAGES

- ▶ Reduces light reflections
- ▶ Increased light extraction efficiency

APPLICATIONS

- ▶ LED manufacturing

This technology is available for licensing.

PATENT STATUS

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OTHER INFORMATION

KEYWORDS

LED, indssl, indled, cenIEE,
indfeat

CATEGORIZED AS

- ▶ **Engineering**
- ▶ **Energy**
 - ▶ Lighting
 - ▶ Other
- ▶ **Semiconductors**
 - ▶ Design and Fabrication

RELATED CASES

2007-670-0

Country	Type	Number	Dated	Case
United States Of America	Issued Patent	8,368,109	02/05/2013	2007-670
United States Of America	Issued Patent	8,124,991	02/28/2012	2007-670

ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

- ▶ Lateral Growth Method for Defect Reduction of Semipolar Nitride Films
- ▶ Vertical Cavity Surface-Emitting Lasers with Continuous Wave Operation
- ▶ Eliminating Misfit Dislocations with In-Situ Compliant Substrate Formation
- ▶ III-Nitride-Based Vertical Cavity Surface Emitting Laser (VCSEL) with a Dielectric P-Side Lens
- ▶ Aluminum-cladding-free Nonpolar III-Nitride LEDs and LDs
- ▶ Low-Cost Zinc Oxide for High-Power-Output, GaN-Based LEDs (UC Case 2010-183)
- ▶ Defect Reduction in GaN films using in-situ SiNx Nanomask
- ▶ Enhanced Light Extraction LED with a Tunnel Junction Contact Wafer Bonded to a Conductive Oxide
- ▶ Low Temperature Deposition of Magnesium Doped Nitride Films
- ▶ Transparent Mirrorless (TML) LEDs
- ▶ Improved GaN Substrates Prepared with Ammonothermal Growth
- ▶ Optimization of Laser Bar Orientation for Nonpolar Laser Diodes
- ▶ Method for Enhancing Growth of Semipolar Nitride Devices
- ▶ Ultraviolet Laser Diode on Nano-Porous AlGaIn template
- ▶ Improved Reliability & Enhanced Performance of III-Nitride Tunnel Junction Optoelectronic Devices
- ▶ Growth of Polyhedron-Shaped Gallium Nitride Bulk Crystals
- ▶ Nonpolar III-Nitride LEDs With Long Wavelength Emission
- ▶ Improved Fabrication of Nonpolar InGaIn Thin Films, Heterostructures, and Devices
- ▶ Growth of High-Quality, Thick, Non-Polar M-Plane GaN Films
- ▶ Method for Growing High-Quality Group III-Nitride Crystals
- ▶ Controlled Photoelectrochemical (PEC) Etching by Modification of Local Electrochemical Potential of Semiconductor Structure
- ▶ Oxyfluoride Phosphors for Use in White Light LEDs
- ▶ Technique for the Nitride Growth of Semipolar Thin Films, Heterostructures, and Semiconductor Devices
- ▶ (In,Ga,Al)N Optoelectronic Devices with Thicker Active Layers for Improved Performance
- ▶ Thermally Stable, Laser-Driven White Lighting Device
- ▶ MOCVD Growth of Planar Non-Polar M-Plane Gallium Nitride
- ▶ Methods for Fabricating III-Nitride Tunnel Junction Devices
- ▶ Low-Droop LED Structure on GaN Semi-polar Substrates
- ▶ Contact Architectures for Tunnel Junction Devices
- ▶ Semi-polar LED/LD Devices on Relaxed Template with Misfit Dislocation at Hetero-interface
- ▶ Semipolar-Based Yellow, Green, Blue LEDs with Improved Performance
- ▶ III-Nitride-Based Devices Grown On Thin Template On Thermally Decomposed Material
- ▶ Growth of Semipolar III-V Nitride Films with Lower Defect Density
- ▶ III-Nitride Tunnel Junction LED with High Wall Plug Efficiency
- ▶ Tunable White Light Based on Polarization-Sensitive LEDs
- ▶ Cleaved Facet Edge-Emitting Laser Diodes Grown on Semipolar GaN
- ▶ Growth of High-Performance M-plane GaN Optical Devices
- ▶ Packaging Technique for the Fabrication of Polarized Light Emitting Diodes
- ▶ Improved Anisotropic Strain Control in Semipolar Nitride Devices
- ▶ Novel Multilayer Structure for High-Efficiency UV and Far-UV Light-Emitting Devices

- ▶ III-V Nitride Device Structures on Patterned Substrates
- ▶ Method for Increasing GaN Substrate Area in Nitride Devices
- ▶ High-Intensity Solid State White Laser Diode
- ▶ Nitride Based Ultraviolet LED with an Ultraviolet Transparent Contact
- ▶ GaN-Based Thermoelectric Device for Micro-Power Generation
- ▶ Limiting Strain-Relaxation in III-Nitride Heterostructures by Substrate Patterning
- ▶ Growth of Planar Semi-Polar Gallium Nitride
- ▶ High-Efficiency and High-Power III-Nitride Devices Grown on or Above a Strain Relaxed Template
- ▶ UV Optoelectronic Devices Based on Nonpolar and Semi-polar AlInN and AlInGaN Alloys
- ▶ Defect Reduction of Non-Polar and Semi-Polar III-Nitrides
- ▶ III-Nitride Based VCSEL with Curved Mirror on P-Side of the Aperture
- ▶ Enhancing Growth of Semipolar (Al,In,Ga,B)N Films via MOCVD

