Low Temperature Deposition of Magnesium Doped Nitride Films
Tech ID: 21919 / UC Case 2006-678-0

BRIEF DESCRIPTION

A method for growing an improved quality device by depositing a low temperature magnesium doped nitride semiconductor thin film.

BACKGROUND

Magnesium doped gallium nitride has been extensively used in nitride based LEDs, but requires high deposition temperatures. Indium nitride has a high volatility and readily evaporates out of the InGaN films when exposed to a high enough temperature or a low temperature for an extended period of time. This time and temperature value is commonly referred to as the material's thermal budget. As a result, there is a need for improved methods for the growth of low temperature magnesium doped nitride planar films, wherein the thermal budget of the previously deposited indium nitride containing multiple quantum wells is considerably reduced.

DESCRIPTION

Researchers at the University of California, Santa Barbara have developed a method for growing an improved quality device by depositing a low temperature magnesium doped nitride semiconductor thin film. This process includes using deposition temperature for the magnesium doped GaN film that is lower than the one used for the deposition of the multi quantum well. This results in a significant increase in the output power of a nitride LED.

ADVANTAGES

▶ Reduced damages to the multi quantum well materials
▶ Increased output power of nitride LED and improved device performance

APPLICATIONS

▶ Nitride LEDs and Laser Diodes

This technology is available for a non-exclusive license. See below for a selection of the patents and patent applications related to this invention. Please inquire for full patent portfolio status.

PATENT STATUS

<table>
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<tr>
<th>Country</th>
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<tr>
<td>United States Of America</td>
<td>Issued Patent</td>
<td>7,709,284</td>
<td>05/04/2010</td>
<td>2006-678</td>
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ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

▶ Enhanced Optical Polarization of Nitride LEDs by Increased Indium Incorporation
▶ Lateral Growth Method for Defect Reduction of Semipolar Nitride Films
▶ Vertical Cavity Surface-Emitting Lasers with Continuous Wave Operation
▶ III-Nitride-Based Devices Grown With Relaxed Active Region
▶ Low-Cost Zinc Oxide for High-Power-Output, GaN-Based LED (UC Case 2010-183)
▶ Defect Reduction in GaN films using in-situ SiNx Nanomask
▶ Enhanced Light Extraction LED with a Tunnel Junction Contact Wafer Bonded to a Conductive Oxide
Highly Efficient Blue-Violet III-Nitride Semipolar Laser Diodes
Hybrid Growth Method for Improved III-Nitride Tunnel Junction Devices
Transparent Mirrorless (TML) LEDs
Improved GaN Substrates Prepared with Ammonothermal Growth
Optimization of Laser Bar Orientation for Nonpolar Laser Diodes
Size-Independent Forward Voltage Micro-LED with an Epitaxial Junction
Method for Enhancing Growth of Semipolar Nitride Devices
III-Nitride Tunnel Junction with Modified Interface
Growth of Polyhedron-Shaped Gallium Nitride Bulk Crystals
Nonpolar III-Nitride LEDs With Long Wavelength Emission
Improved Fabrication of Nonpolar InGaN Thin Films, Heterostructures, and Devices
Growth of High-Quality, Thick, Non-Polar M-Plane GaN Films
Increased Light Extraction with Multistep Deposition of ZnO on GaN
Method for Manufacturing Improved III-Nitride LEDs and Laser Diodes: Monolithic Integration of Optically Pumped and Electrically Injected III-Nitride LEDs
Selective-Area Mesoporous Semiconductors And Devices For Optoelectronic And Photonic Applications
High-Efficiency, Mirrorless Non-Polar and Semi-Polar Light Emitting Devices
Method for Growing High-Quality Group III-Nitride Crystals
Controlled Photoelectrochemical (PEC) Etching by Modification of Local Electrochemical Potential of Semiconductor Structure
Oxyfluoride Phosphors for Use in White Light LEDs
Technique for the Nitride Growth of Semipolar Thin Films, Heterostructures, and Semiconductor Devices
(In,Ga,AI)N Optoelectronic Devices with Thicker Active Layers for Improved Performance
Thermally Stable, Laser-Driven White Lighting Device
MOCVD Growth of Planar Non-Polar M-Plane Gallium Nitride
Reduced Dislocation Density of Non-Polar GaN Grown by Hydride Vapor Phase Epitaxy
Highly Compact, High-Index Dielectric Nanostructures for Deep-Ultraviolet Devices
Reduction in Leakage Current and Increase in Efficiency of III-Nitride MicroLEDs
Methods for Fabricating III-Nitride Tunnel Junction Devices
Low-Droop LED Structure on GaN Semi-polar Substrates
Contact Architectures for Tunnel Junction Devices
Semipolar LED/LD Devices on Relaxed Template with Mismatch Dislocation at Hetero-interface
Photoelectrochemical Etching Of P-Type Semiconductor Heterostructures
Semipolar-Based Yellow, Green, Blue LEDs with Improved Performance
III-Nitride-Based Devices Grown On Thin Template On Thermally Decomposed Material
Growth of Semipolar III-V Nitride Films with Lower Defect Density
III-Nitride Tunnel Junction LED with High Wall Plug Efficiency
Improved Manufacturing of Solid State Lasers via Patterning of Photonic Crystals
High Efficiency III-Nitride Devices with Smooth Relaxed InGaN Buffer and Strain Compliant Template
Tunable White Light Based on Polarization-Sensitive LEDs
Cleaved Facet Edge-Emitting Laser Diodes Grown on Semipolar GaN
Growth of High-Performance M-plane GaN Optical Devices
Packaging Technique for the Fabrication of Polarized Light Emitting Diodes
Improved Anisotropic Strain Control in Semipolar Nitride Devices
High Light Extraction Efficiency III-Nitride LED
III-V Nitride Device Structures on Patterned Substrates
Activation of P-Type Layers of Tunnel Junctions in Micro-LEDs
Method for Increasing GaN Substrate Area in Nitride Devices
Nitride Based Ultraviolet LED with an Ultraviolet Transparent Contact
Growth of Planar, Non-Polar, A-Plane GaN by Hydride Vapor Phase Epitaxy
GaN-Based Thermoelectric Device for Micro-Power Generation
Limiting Strain-Relaxation in III-Nitride Heterostructures by Substrate Patterning
LED Device Structures with Minimized Light Re-Absorption
Growth of Planar Semi-Polar Gallium Nitride
Nonpolar (Al, B, In, Ga)N Quantum Well Design
UV Optoelectronic Devices Based on Nonpolar and Semi-polar AlInN and AlInGaN Alloys
Defect Reduction of Non-Polar and Semi-Polar III-Nitrides
III-Nitride Based VCSEL with Curved Mirror on P-Side of the Aperture