Lateral Growth Method for Defect Reduction of Semipolar Nitride Films

Tech ID: 21918 / UC Case 2005-672-0

BRIEF DESCRIPTION

A novel method for defect reduction via lateral growth of semipolar nitrides

BACKGROUND

As bulk GaN crystals are not widely available, current devices are grown on foreign substrates heteroepitaxially. The nature of heteroepitaxial growth leads to significant defect densities, most prominently in the form of threading dislocations. Researchers are continually trying to reduce defect density. In c-plane nitride growth, as well as other semiconductor materials systems, the threading dislocation defects predominantly propagate along the principal growth direction. As such, laterally growing polar and nonpolar nitrides tend to exhibit reduced defect densities.

DESCRIPTION

Researchers at the University of California, Santa Barbara have developed a novel method for defect reduction via lateral growth of semipolar nitrides. Lateral growth can be used to reduce defect density in semipolar nitride films by such growth techniques as LEO, SLEO, cantilever epitaxy, and nanomasking. The lateral growth can also be performed multiple times to further decrease the dislocation density.

ADVANTAGES

- Reduced defect density in semipolar nitride films
- Can be performed multiple times to further decrease dislocation density

APPLICATIONS

- Growth of semipolar nitride films

This technology is available for a non-exclusive license. See below for a selection of the patents and patent applications related to this invention. Please inquire for full patent portfolio status.

PATENT STATUS

Country	Type	Number	Dated	Case
United States Of America	Issued Patent	8,148,244	04/03/2012	2005-672

CONTACT

Pasquale S. Ferrari
ferrari@tia.ucsb.edu
tel: .

INVENTORS

- Baker, Troy J.
- Haskell, Benjamin A.
- Nakamura, Shuji
- Speck, James S.

OTHER INFORMATION

KEYWORDS

nitride films, indssl, indbulk, cenIEE

CATEGORIZED AS

- **Engineering**
- **Optics and Photonics**
  - All Optics and Photonics
- **Semiconductors**
  - Design and Fabrication

RELATED CASES

2005-672-0

ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

- Etching Technique for the Fabrication of Thin (Al, In, Ga)N Layers
- Vertical Cavity Surface-Emitting Lasers with Continuous Wave Operation
- Eliminating Misfit Dislocations with In-Situ Compliant Substrate Formation
- III-Nitride-Based Vertical Cavity Surface Emitting Laser (VCSEL) with a Dielectric P-Side Lens
- Aluminum-cladding-free Nonpolar III-Nitride LEDs and LDs
- Low-Cost Zinc Oxide for High-Power-Output, GaN-Based LEDs (UC Case 2010-183)
- Defect Reduction in GaN films using in-situ SiNx Nanomask
- Enhanced Light Extraction LED with a Tunnel Junction Contact Wafer Bonded to a Conductive Oxide
- Low Temperature Deposition of Magnesium Doped Nitride Films
- Transparent Mirrorless (TML) LEDs
- Improved GaN Substrates Prepared with Ammonothermal Growth
- Optimization of Laser Bar Orientation for Nonpolar Laser Diodes
- Method for Enhancing Growth of Semipolar Nitride Devices
- Ultraviolet Laser Diode on Nano-Porous AlGaN template
- Growth of Polyhedron-Shaped Gallium Nitride Bulk Crystals
- Nonpolar III-Nitride LEDs With Long Wavelength Emission
- Improved Fabrication of Nonpolar InGaN Thin Films, Heterostructures, and Devices
- Growth of High-Quality, Thick, Non-Polar M-Plane GaN Films
- Method for Growing High-Quality Group III-Nitride Crystals
- Controlled Photoelectrochemical (PEC) Etching by Modification of Local Electrochemical Potential of Semiconductor Structure
- Technique for the Nitride Growth of Semipolar Thin Films, Heterostructures, and Semiconductor Devices
- MOCVD Growth of Planar Non-Polar M-Plane Gallium Nitride
- Methods for Fabricating III-Nitride Tunnel Junction Devices
- Low-Droop LED Structure on GaN Semi-polar Substrates
- Contact Architectures for Tunnel Junction Devices
- Semi-polar LED/LD Devices on Relaxed Template with Misfit Dislocation at Hetero-interface
- Semipolar-Based Yellow, Green, Blue LEDs with Improved Performance
- III-Nitride-Based Devices Grown On Thin Template On Thermally Decomposed Material
- Growth of Semipolar III-V Nitride Films with Lower Defect Density
- III-Nitride Tunnel Junction LED with High Wall Plug Efficiency
- Tunable White Light Based on Polarization-Sensitive LEDs
- Cleaved Facet Edge-Emitting Laser Diodes Grown on Semipolar GaN
- Growth of High-Performance M-plane GaN Optical Devices
- Packaging Technique for the Fabrication of Polarized Light Emitting Diodes
- Improved Anisotropic Strain Control in Semipolar Nitride Devices
- Novel Multilayer Structure for High-Efficiency UV and Far-UV Light-Emitting Devices
- III-V Nitride Device Structures on Patterned Substrates
- Method for Increasing GaN Substrate Area in Nitride Devices
- High-Intensity Solid State White Laser Diode
- Nitride Based Ultraviolet LED with an Ultraviolet Transparent Contact
- GaN-Based Thermoelectric Device for Micro-Power Generation
- Limiting Strain-Relaxation in III-Nitride Heterostructures by Substrate Patterning
- LED Device Structures with Minimized Light Re-Absorption
- Growth of Planar Semi-Polar Gallium Nitride

- High-Efficiency and High-Power III-Nitride Devices Grown on or Above a Strain Relaxed Template
- UV Optoelectronic Devices Based on Nonpolar and Semi-polar AlInN and AlInGaN Alloys
- Defect Reduction of Non-Polar and Semi-Polar III-Nitrides
- III-Nitride Based VCSEL with Curved Mirror on P-Side of the Aperture
- Enhancing Growth of Semipolar (Al,In,Ga,B)N Films via MOCVD

University of California, Santa Barbara
Office of Technology & Industry Alliances
342 Lagoon Road, ,Santa Barbara,CA 93106-2055 |
www.tia.ucsb.edu
Tel: 805-893-2073 | Fax: 805.893.5236 | padilla@tia.ucsb.edu