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Tech ID: 30035



ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

- ▶ Methods And Systems For Magnetoelectronic Elements And Arrays
- ▶ Vertical-Stacked-Array-Transistor (VSAT) for Nonvolatile Memory Devices
- ▶ Magnetic Memory Bits with Perpendicular Magnetization Switched By Current-Induced Spin-Orbit Torques
- ▶ Vsat Structure for Nonvolatile Memory Device
- ▶ A Read-Disturbance-Free Nonvolatile Content Adressable Memory
- ▶ A Self-Organized Critical CMOS Circuit for Computation and Information Processing
- ▶ Anti-Ferromagnetic Magneto-Electric Spin-Orbit Read Logic

Gateway to Innovation, Research and Entrepreneurship

UCLA Technology Development Group

10889 Wilshire Blvd., Suite 920, Los Angeles, CA 90095

<https://tdg.ucla.edu>

Tel: 310.794.0558 | Fax: 310.794.0638 | ncd@tdg.ucla.edu

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