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Robust High Speed Analog QAM Demodulator for Advanced Wireless Applications

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BRIEF DESCRIPTION

Wireless applications are witnessing major advancement in fields like virtual reality and cellular phones, thus requiring much higher data transfer speed. This technology is a novel architecture for wireless receivers that accommodates such targeted high data rates, while maintaining a cost efficient design; power efficient while still utilizing simple circuits design, through replacing complicated digital blocks with innovative analog ones.

FULL DESCRIPTION

Technology is undergoing major developments, moving towards real-time high quality application. Accordingly, recent need for high speed wireless data transfer has been identified for applications like virtual reality multi-gigabit-per-second data transfer systems. With current systems, it is not possible to accommodate such demanding increases with the current capabilities of digital wireless transmission systems, as bandwidth expansion is no longer a realistic option due to band crowding below 30GHz. Furthermore, digital wireless communication systems at speeds greater than 10 Gbps would be impractical and inefficient due to a high degree of circuit complexity and excessive power consumption needs. A simplified, power efficient high-speed wireless communication methodology is critical to accommodate the emerging high speed wireless applications.

UCI researchers have developed a simplified analog approach to high speed wireless communication. The novel receiver demodulates the signal directly in the analog domain, eliminating the need for analog to digital conversion followed by the digital processing. With such simplified circuitry, power requirements are reduced, chip area is minimized, yet signal demodulation speeds are increased.

SUGGESTED USES

- High speed wireless data transfer systems:
 - » Virtual reality applications
 - » Mobile communications
- Reduce the energy requirement for high speed signal communication

ADVANTAGES

- » Power efficiency improved as a result of the reduced design complexity of the analog QAM design
- » Increased wireless communication speed for cellular or personal computing
- » Automatic tracking of gain variations

STATE OF DEVELOPMENT

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OTHER INFORMATION

CATEGORIZED AS

- » **Communications**
 - » Networking
 - » Wireless
- » **Computer**
 - » Hardware

RELATED CASES

2017-984-0

System has been designed and validated using computer simulations (showing the Bit error rates as well as the power performance)

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