Methods for Fabricating III-Nitride Tunnel Junction Devices
Tech ID: 28865 / UC Case 2017-130-0

BRIEF DESCRIPTION
Methods of physical vapor deposition for III-nitride tunnel junction devices.

BACKGROUND
Current commercially-available III-Nitride light-emitting diodes (LEDs) and edge-emitting laser diodes (EELDs) use an active region in a biased p-n junction to allow for electron and hole injection. However, since p-type gallium nitride (p-GaN) is difficult to contact electrically and has low hole concentration and mobility, p-GaN cannot be used as a current spreading layer and traditional p-contacts will add significant voltage to devices. Despite these inherent problems, all commercial light emitting devices utilize a p-contact and a material other than p-GaN for current spreading, typically transparent conducting oxides (TCO).

A tunnel junction is a diode comprised of a very highly doped (n+/p+) interface that allows for electrons to tunnel between the valence band conduction band. Although in principle a highly doped Esaki-type homojunction diode should provide the lowest-loss tunnel junction, there have been a number of difficulties achieving high quality tunnel junctions in the GaN material system.

DESCRIPTION
Researchers at the University of California, Santa Barbara have satisfied the need for improving the performance of III-nitride devices by overcoming the limitations currently in the field. Improvements include alternative methods for creating tunnel junction devices, including physical vapor deposition (e.g., sputter deposition) and MOCVD regrowth deposition, and III-Nitride tunnel junction improvement through reduction of the magnesium memory effect. Self-emissive III-V micro LED-based displays using low resistance tunnel junctions leveraging these improvements have been fabricated.

ADVANTAGES
▶ Eliminates the need for a TCO or silver mirror
▶ Easy wide scale adoption of sputter deposition tools
▶ Reduction of excess magnesium in the fabrication of III-nitride junctions
▶ Fabrication of III-nitride tunnel junction devices using MOCVD for growth

APPLICATIONS
▶ LEDs
▶ Tunnel Junctions
▶ III-nitride devices

PATENT STATUS

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<th>Country</th>
<th>Type</th>
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<td>United States Of America</td>
<td>Published Application</td>
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RELATED TECHNOLOGIES
▶ Laser Diode With Tunnel Junction Contact Surface Grating
▶ Contact Architectures for Tunnel Junction Devices
▶ III-Nitride Tunnel Junction LED with High Wall Plug Efficiency
ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

- Method for Improved Surface of (Ga,Al,In,B)N Films on Nonpolar or Semipolar Substrates
- High Efficiency LED with Optimized Photonic Crystal Extractor
- Enhanced Optical Polarization of Nitride LEDs by Increased Indium Incorporation
- Etching Technique for the Fabrication of Thin (Al, In, Ga)N Layers
- Lateral Growth Method for Defect Reduction of Semipolar Nitride Films
- Vertical Cavity Surface-Emitting Lasers with Continuous Wave Operation
- Low-Cost Zinc Oxide for High-Power-Output, GaN-Based LEDs (UC Case 2010-183)
- Internal Heating for Ammonothermal Growth of Group-III Nitride Crystals
- Defect Reduction in GaN films using in-situ SiNx Nanomask
- Enhanced Light Extraction LED with a Tunnel Junction Contact Wafer Bonded to a Conductive Oxide
- Highly Efficient Blue-Violet III-Nitride Semipolar Laser Diodes
- Hybrid Growth Method for Improved III-Nitride Tunnel Junction Devices
- Phosphor-Free White Light Source
- Volumetric Hole Injection with Intentional V-Defects
- Low Temperature Deposition of Magnesium Doped Nitride Films
- Transparent Mirrorless (TML) LEDs
- Improved GaN Substrates Prepared with Ammonothermal Growth
- Laser Diode With Tunnel Junction Contact Surface Grating
- Optimization of Laser Bar Orientation for Nonpolar Laser Diodes
- High Efficiency Semipolar AlGaN-Cladding-Free Laser Diodes
- Method for Growing Self-Assembled Quantum Dot Lattices
- Method for Enhancing Growth of Semipolar Nitride Devices
- III-Nitride Tunnel Junction with Modified Interface
- Growth of Polyhedron-Shaped Gallium Nitride Bulk Crystals
- Nonpolar III-Nitride LEDs With Long Wavelength Emission
- Improved Fabrication of Nonpolar InGaN Thin Films, Heterostructures, and Devices
- Growth of High-Quality, Thick, Non-Polar M-Plane GaN Films
- Increased Light Extraction with Multistep Deposition of ZnO on GaN
- Method for Manufacturing Improved III-Nitride LEDs and Laser Diodes: Monolithic Integration of Optically Pumped and Electrically Injected III-Nitride LEDs
- Selective-Area Mesoporous Semiconductors And Devices For Optoelectronic And Photonic Applications
- High-Efficiency, Mirrorless Non-Polar and Semi-Polar Light Emitting Devices
- Method for Growing High-Quality Group III-Nitride Crystals
- Controlled Photoelectrochemical (PEC) Etching by Modification of Local Electrochemical Potential of Semiconductor Structure
- Incorporating Temperature-Sensitive Layers in III-N Devices
- Oxyfluoride Phosphors for Use in White Light LEDs
- Technique for the Nitride Growth of Semipolar Thin Films, Heterostructures, and Semiconductor Devices
- (In,Ga,Al)N Optoelectronic Devices with Thicker Active Layers for Improved Performance
- MOCVD Growth of Planar Non-Polar M-Plane Gallium Nitride
- Reduced Dislocation Density of Non-Polar GaN Grown by Hydride Vapor Phase Epitaxy
- Heterogeneously Integrated GaN on Si Photonic Integrated Circuits
- Reduction in Leakage Current and Increase in Efficiency of III-Nitride MicroLEDS
- Low-Droop LED Structure on GaN Semi-polar Substrates
- Contact Architectures for Tunnel Junction Devices
- Semi-polar LED/LD Devices on Relaxed Template with Misfit Dislocation at Hetero-interface
- Photoelectrochemical Etching Of P-Type Semiconductor Heterostructures
- Semipolar-Based Yellow, Green, Blue LEDs with Improved Performance
- Growth of Semipolar III-V Nitride Films with Lower Defect Density
- III-Nitride Tunnel Junction LED with High Wall Plug Efficiency
- Improved Manufacturing of Solid State Lasers via Patternning of Photonic Crystals
- Solid Solution Phosphors for Use in Solid State White Lighting Applications
- Multifaceted III-Nitride Surface-Emitting Laser
- Tunable White Light Based on Polarization-Sensitive LEDs
Cleaved Facet Edge-Emitting Laser Diodes Grown on Semipolar GaN
- Growth of High-Performance M-plane GaN Optical Devices
- Packaging Technique for the Fabrication of Polarized Light Emitting Diodes
- Improved Anisotropic Strain Control in Semipolar Nitride Devices
- High Light Extraction Efficiency III-Nitride LED
- Photoelectrochemical Etching for Chip Shaping Of LEDs
- III-V Nitride Device Structures on Patterned Substrates
- Hexagonal Wurtzite Type Epitaxial Layer with a Low Alkali-Metal Concentration
- Method for Increasing GaN Substrate Area in Nitride Devices
- Growth of Planar, Non-Polar, A-Plane GaN by Hydride Vapor Phase Epitaxy
- Single or Multi-Color High Efficiency LED by Growth Over a Patterned Substrate
- GaN-Based Thermoelectric Device for Micro-Power Generation
- Limiting Strain-Relaxation in III-Nitride Heterostructures by Substrate Patterning
- Improved Manufacturing of Semiconductor Lasers
- LED Device Structures with Minimized Light Re-Absorption
- Growth of Planar Semi-Polar Gallium Nitride
- Nonpolar (Al, B, In, Ga)N Quantum Well Design
- UV Optoelectronic Devices Based on Nonpolar and Semi-polar AlInN and AlInGaN Alloys
- Integration And Mass Transfer Of Microleds
- Defect Reduction of Non-Polar and Semi-Polar III-Nitrides
- III-Nitride Based VCSEL with Curved Mirror on P-Side of the Aperture
- Low-Cost Zinc Oxide for High-Power-Output, GaN-Based LEDs (UC Case 2010-150)
- Suppression of Defect Formation and Increase in Critical Thickness by Silicon Doping
- Wafer Bonding for Embedding Active Regions with Relaxed Nanofeatures
- Enhancing Growth of Semipolar (Al,In,Ga,B)N Films via MOCVD