

ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

- ▶ [Methods for Integrated Circuit C4 Ball Placement Considering Package Reliability](#)
- ▶ [Distributed Energy Conserving LC Resonant Clock Trees](#)
- ▶ [High-Performance Clock Grid Synthesis and Tuning Using Distributed LC Resonant Tanks](#)
- ▶ [Current-Mode Clock Distribution](#)

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