

Request Information

Permalink

## This technology is currently not available for licensing

Tech ID: 22141



### ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

- ▶ Vertical-Stacked-Array-Transistor (VSAT) for Nonvolatile Memory Devices
- ▶ Magnetic Memory Bits with Perpendicular Magnetization Switched By Current-Induced Spin-Orbit Torques
- ▶ Vsat Structure for Nonvolatile Memory Device
- ▶ A Read-Disturbance-Free Nonvolatile Content Addressable Memory
- ▶ A Self-Organized Critical CMOS Circuit for Computation and Information Processing
- ▶ Anti-Ferromagnetic Magneto-Electric Spin-Orbit Read Logic

## Gateway to Innovation, Research and Entrepreneurship

### UCLA Technology Development Group

10889 Wilshire Blvd., Suite 920, Los Angeles, CA 90095

tdg.ucla.edu

Tel: 310.794.0558 | Fax: 310.794.0638 | ncd@tdg.ucla.edu

© 2011 - 2014, The Regents of the University of California

[Terms of use](#)

[Privacy Notice](#)

