

# New Methods for Write-Once Memory, Other Memory Devices, and General Error-Correcting Codes

Tech ID: 21587 / UC Case 2010-326-0

## TECHNOLOGY DESCRIPTION

UC San Diego inventors have developed error-correcting codes that allow for double- and triple-error correction. These codes can be applied to communications and memory storage, especially for write-once memory.

Additionally, UC San Diego inventors have developed codes for write-once memory that allows for two writes in an efficient manner. This invention can be applied to make more efficient use of flash memory, or other types of write-once memory devices.

The research behind these inventions can be reviewed at these links.

- [http://itw2010dublin.org/cs\\_memories.htm](http://itw2010dublin.org/cs_memories.htm)
- [http://www.eurekalert.org/pub\\_releases/2010-10/uoc--usd102510.php](http://www.eurekalert.org/pub_releases/2010-10/uoc--usd102510.php)

## PATENT STATUS

Country	Type	Number	Dated	Case
United States Of America	Issued Patent	9,141,474	09/22/2015	2010-327
United States Of America	Issued Patent	8,977,936	03/10/2015	2010-326

## CONTACT

University of California, San Diego  
Office of Innovation and Commercialization  
[innovation@ucsd.edu](mailto:innovation@ucsd.edu)  
tel: 858.534.5815.



## OTHER INFORMATION

### KEYWORDS

WOM, flash memory, error correction

### CATEGORIZED AS

- **Computer**
- Other
- Software

### RELATED CASES

2010-326-0, 2010-327-0

University of California, San Diego  
Office of Innovation and Commercialization  
9500 Gilman Drive, MC 0910, ,  
La Jolla, CA 92093-0910

Tel: 858.534.5815  
[innovation@ucsd.edu](mailto:innovation@ucsd.edu)  
<https://innovation.ucsd.edu>  
Fax: 858.534.7345

© 2011 - 2015, The Regents  
of the University of  
California  
[Terms of use](#)  
[Privacy Notice](#)