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A Novel Multi-Stage Motion Vector Processing and Application in Motion Compensated Frame Interpolation

Tech ID: 20668 / UC Case 2007-150-0

TECHNOLOGY DESCRIPTION

UC San Diego researchers have invented a method for motion vector correction for motion-compensated frame interpolation for 120Hz LCD display, enhanced video decoders, as well as any applications that requires frame rate conversion. The method accomplishes the concept of object motion without complex motion estimation and can eliminate blocking artifact using smaller block size. Moreover, this invention outperforms other conventional methods on both objective and subjective video quality.

PATENT STATUS

Country	Туре	Number	Dated	Case
United States Of America	Issued Patent	8,605,786	12/10/2013	2007-150

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INVENTORS

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OTHER INFORMATION

KEYWORDS

image processing, motion estimation,

frame rate conversion, compression,

motion vector processing, 120Hz LCD

CATEGORIZED AS

Communications

Wireless

RELATED CASES 2007-150-0

ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

New Global Motion Algorithm

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