

ADDITIONAL TECHNOLOGIES BY THESE INVENTORS

- ▶ [Methods for Integrated Circuit C4 Ball Placement Considering Package Reliability](#)
- ▶ [Distributed Energy Conserving LC Resonant Clock Trees](#)
- ▶ [High-Performance Clock Grid Synthesis and Tuning Using Distributed LC Resonant Tanks](#)
- ▶ [Current-Mode Clock Distribution](#)

University of California, Santa Cruz Industry Alliances & Technology Commercialization Kerr 413 / IATC, Santa Cruz,CA 95064	Tel: 831.459.5415 innovation@ucsc.edu officeofresearch.ucsc.edu/ Fax: 831.459.1658	© 2012 - 2018, The Regents of the University of California Terms of use Privacy Notice
--	--	--